

MICRO ELECTRONICS

2N4400
2N4401

THE 2N4400, 2N4401 ARE NPN SILICON PLANAR EPITAXIAL TRANSISTORS FOR GENERAL PURPOSE AMPLIFIERS AND MEDIUM SPEED SWITCHING APPLICATIONS. THEY ARE COMPLEMENTARY TO THE PNP TYPE 2N4402 AND 2N4403 RESPECTIVELY.

CASE TO-92A



EBC

ABSOLUTE MAXIMUM RATINGS

Collector-Base Voltage	V _{CBO}	60V
Collector-Emitter Voltage	V _{CEO}	40V
Emitter-Base Voltage	V _{EBO}	6V
Collector Current	I _C	0.6A
Total Power Dissipation (T _A ≤ 25°C)	P _{tot}	500mW **
Operating Junction & Storage Temperature	T _j , T _{stg}	-55 to 150°C

** 310mW in JEDEC registration.

ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	2N4400		2N4401		UNIT	TEST CONDITIONS
		MIN	MAX	MIN	MAX		
Collector-Base Breakdown Voltage	V _{CBO}	60		60		V	I _C =0.1mA I _E =0
Collector-Emitter Breakdown Voltage	V _{CEO} *	40		40		V	I _C =1mA I _B =0
Emitter-Base Breakdown Voltage	V _{EBO}	6		6		V	I _E =0.1mA I _C =0
Collector Cutoff Current	I _{CEV}		0.1		0.1	μA	V _{CE} =35V V _{EB} =0.4V
Base Cutoff Current	I _{BL}		0.1		0.1	μA	V _{CE} =35V V _{EB} =0.4V
Collector-Emitter Saturation Voltage	V _{CE(sat)} *		0.4		0.4	V	I _C =150mA I _B =15mA
			0.75		0.75	V	I _C =500mA I _B =50mA
Base-Emitter Saturation Voltage	V _{BE(sat)} *	0.75	0.95	0.75	0.95	V	I _C =150mA I _B =15mA
			1.2		1.2	V	I _C =500mA I _B =50mA
D.C. Current Gain	H _{FE} *				20		I _C =0.1mA V _{CE} =1V
					40		I _C =1mA V _{CE} =1V
					80		I _C =10mA V _{CE} =1V
			150	100	300		I _C =150mA V _{CE} =1V
					40		I _C =500mA V _{CE} =2V
Current Gain-Bandwidth Product	f _T	200		250		MHz	I _C =20mA V _{CE} =10V

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PARAMETER	SYMBOL	2N4400		2N4401		UNIT	TEST CONDITIONS
		MIN	MAX	MIN	MAX		
Collector-Base Capacitance	Cob		6.5		6.5	pF	V _{CB} =5V I _E =0 f=140kHz
Emitter-Base Capacitance	Cib		30		30	pF	V _{EB} =0.5V I _C =0 f=140kHz
Input Impedance	h _{ie}	0.5	7.5	1.0	15	KΩ	I _C =1mA V _{CE} =10V f=1kHz
Voltage Feedback Ratio	h _{re}	0.1	8.0	0.1	8.0	x10 ⁻⁴	I _C =1mA V _{CE} =10V f=1kHz
Small Signal Current Gain	h _{fe}	20	250	40	500		I _C =1mA V _{CE} =10V f=1kHz
Output Admittance	h _{oe}	1	30	1	30	μS	I _C =1mA V _{CE} =10V f=1kHz
Delay Time	t _d		15		15	nS	I _C =150mA I _{B1} =15mA V _{CC} =30V
Rise Time	t _r		20		20	nS	I _C =150mA I _{B1} =15mA V _{CC} =30V
Storage Time	t _s		225		225	nS	I _C =150mA I _{B1} =-I _{B2} =15mA V _{CC} =30V
Fall Time	t _f		30		30	nS	I _C =150mA I _{B1} =-I _{B2} =15mA V _{CC} =30V

* Pulse Test : Pulse Width=0.3mS, Duty Cycle=1%

