

PLASTIC SILICON RECTIFIERS

REVERSE VOLTAGE - **50 to 1000** Volts
FORWARD CURRENT - **3.0** Amperes

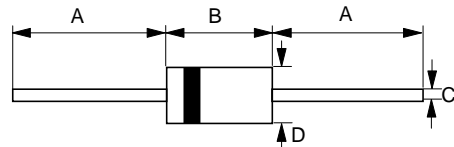
FEATURES

- Low cost
- Diffused junction
- Low forward voltage drop
- Low reverse leakage current
- High current capability
- The plastic material carries UL recognition 94V-0

MECHANICAL DATA

- Case : JEDEC DO-201AD molded plastic
- Polarity : Color band denotes cathode
- Weight : 0.04 ounces, 1.1 grams
- Mounting position : Any

DO-201AD



DO-201AD		
Dim.	Min.	Max.
A	25.4	-
B	7.30	9.50
C	1.20	1.30
D	4.80	5.30
All Dimensions in millimeter		

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%

CHARACTERISTICS	SYMBOL	1N	1N	1N	1N	1N	1N	1N	1N	1N	UNIT
		5400	5401	5402	5403	5404	5405	5406	5407	5408	
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	300	400	500	600	800	1000	V
Maximum RMS Voltage	V _{RMS}	35	70	140	210	280	350	420	560	700	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	300	400	500	600	800	1000	V
Maximum Average Forward Rectified Current @T _A =55°C	I _(AV)	3.0									A
Peak Forward Surge Current 8.3ms single half sine-wave super imposed on rated load (JEDEC METHOD)	I _{FSM}	200									A
Maximum forward Voltage at 3.0A DC	V _F	1.0									V
Maximum DC Reverse Current at Rated DC Blocking Voltage @T _J =25°C @T _J =100°C	I _R	10 50									uA
Typical Junction Capacitance (Note 1)	C _J	50					35				pF
Typical Thermal Resistance (Note 2)	R _{θJA}	15									°C/W
Operating Temperature Range	T _J	-55 to +125									°C
Storage Temperature Range	T _{STG}	-55 to +150									°C

NOTES : 1. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
2. Thermal Resistance Junction to Ambient.

REV. 2, 01-Dec-2000, KDAF01

