

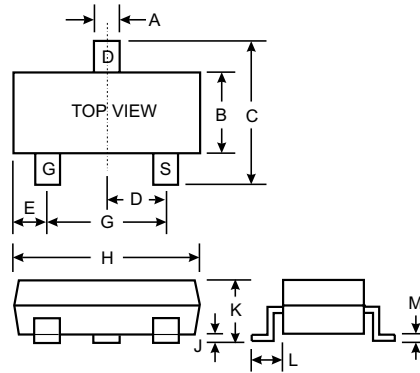
N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance: $R_{DS(ON)}$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage

Mechanical Data

- Case: SOT-23, Molded Plastic
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking: K7A
- Weight: 0.008 grams (approx.)



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.19	1.40
C	2.10	2.50
D	0.89	1.05
E	0.45	0.61
G	1.78	2.05
H	2.65	3.05
J	0.013	0.15
K	0.89	1.10
L	0.45	0.61
M	0.076	0.178
All Dimensions in mm		

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V_{DSS}	60	V
Drain-Gate Voltage $R_{GS} \leq 1.0\text{M}\Omega$	V_{DGR}	60	V
Gate-Source Voltage	V_{GSS}	± 20 ± 40	V
Drain Current (Note 1)	I_D	115 73 800	mA
Total Power Dissipation (Note 1) Derating above $T_A = 25^\circ\text{C}$	P_d	200 1.60	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	625	K/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

- Note: 1. Valid provided that terminals are kept at specified ambient temperature.
 2. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 2)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	70	—	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1.0 500	μA	V _{DS} = 60V, V _{GS} = 0V @ T _C = 25°C @ T _C = 125°C
Gate-Body Leakage	I _{GSS}	—	—	±10	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 2)						
Gate Threshold Voltage	V _{GS(th)}	1.0	—	2.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	3.2 4.4	7.5 13.5	Ω	V _{GS} = 5.0V, I _D = 0.05A V _{GS} = 10V, I _D = 0.5A @ T _j = 25°C @ T _j = 125°C
On-State Drain Current	I _{D(ON)}	—	1.0	0.5	A	V _{GS} = 10V, V _{DS} = 7.5V
Forward Transconductance	g _{FS}	80	—	—	mS	V _{DS} = 10V, I _D = 0.2A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	22	50	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	11	25	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.0	5.0	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{D(ON)}	—	7.0	20	ns	V _{DD} = 30V, I _D = 0.2A, R _L = 150Ω, V _{GEN} = 10V, R _{GEN} = 25Ω
Turn-Off Delay Time	t _{D(OFF)}	—	11	20	ns	

Note: 1. Valid provided that terminals are kept at specified ambient temperature.

2. Pulse width ≤ 300μs, duty cycle ≤ 2%.

